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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: A. TANAKA et al.  
Serial No.: 09/712,171  
Filed: November 15, 2000  
For: DATA RECONSTRUCTION METHOD AND SYSTEM  
WHEREIN TIMING OF DATA RECONSTRUCTION IS  
CONTROLLED IN ACCORDANCE WITH CONDITIONS  
WHEN A FAILURE OCCURS. (As Amended)  
Art Unit: 2184  
Examiner: S. Baderman

**RECEIVED**

AUG 26 2002

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Box RCE  
Assistant Commissioner for Patents  
Washington, D.C. 20231

August 23, 2002

Sir:

The Notice of Allowance dated May 30, 2002, in connection with the above-identified application is acknowledged. The issue fee has not yet been paid. A request for continued examination (RCE) under 37 CFR 1.114 is being submitted herewith.

Pursuant to 37 CFR 1.97(b) and 1.98, attached hereto are copies of U.S. Patent Nos. 4,053,752, 4,358,848, 4,914,656, 4,958,351, 5,274,799; Japanese references 63-155468 and 5-2516 (each including an English abstract); and the following six publications:

W. Meador, "Disk array systems", Digest of Papers, Compcon '89: Thirty-Fourth IEEE Computer Society International Conference, February 27, 1989, San Francisco, CA, pp. 143-146 (abstract only).

G. Gibson et al., "Failure correction techniques for large disk arrays", Proceedings of the Third International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS-III), April 3-6, 1989, Boston, MA, pp. 123-132 (abstract only).

T. Olson, "Disk array performance in a random IO environment", Computer Architecture News, Vol. 17, No. 5, September 1989, pp. 71-77 (abstract only).

R. Katz et al., "Disk System Architectures for High Performance Computing", Proceedings of the IEEE, Vol. 77, No. 12, December 1989, pp. 1842-1858.

P. Chen et al., "Maximizing performance in a striped disk array", Proceedings of the 17th Annual Symposium on Computer Architecture, May 28-31, 1990, Seattle, WA, pp. 322-331 (abstract only).

A. Reddy et al., "Gracefully Degradable Disk Arrays", Proceedings of the 1991 International Symposium on Fault-Tolerant Computing (FTCS 1991), June 25-27, 1991, Montreal, Canada, pp. 401-408.

Also attached hereto are three forms PTO-1449 listing the thirteen references listed above.

Japanese references 63-155468 and 5-2516 are not in the English language.

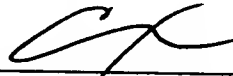
The concise explanation of the relevance of Japanese references 63-155468 and 5-2516 required by 37 CFR 1.98(a)(3)(i) is provided by the English abstracts of these references.

Pursuant to 37 CFR 1.97(b)(4), this Information Disclosure Statement is being before the mailing of a first Office Action after the filing of a request for continued examination (RCE) under 37 CFR 1.114.

It is respectfully requested that this Information Disclosure Statement be considered.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP



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Carl I. Brundidge  
Registration No. 29,621

CIB/RSS  
(703) 312-6600

Attachments